22423

2	222	3													
3	Ho	ours	/	70	Marks	Seat	No.								
	Instru	ctions	_	(1)	All Question	s are Comp	ulsor	y.							
				(2)	Answer each	next main	Que	stic	on	on	a ne	ew	pag	ge.	
				(3)	Illustrate you necessary.	ir answers v	with	nea	at s	ket	ches	wl	here	ever	
				(4)	Figures to the	e right indi	cate	fu	ll n	nark	S.				
				(5)	Assume suita	able data, if	nece	ess	ary.						
				(6)	Use of Non- Calculator is	programmab permissible	ole E	lec	etror	nic	Poc	ket			
				(7)	Mobile Phon Communicati Examination	e, Pager an on devices Hall	d any are r	y (not	othe	er E rmi	lect	ron: le i	ic n		
					LAummuton	11411.								Ma	rks
1.		Atter	npt	any	<u>FIVE</u> of the	e following:									10
	a)	Defir	ne :												
		i)	Sle	w ra	te										
		ii)	CM	1RR											
	b)	Draw	ci	rcuit	diagram of n	on-inverting	adde	er	wit	h 3	inp	outs			
	c)	List	any	four	specifications	s of IC LM	[-324.	•							
	d)	Give	cla	ssific	ation of filter	S.									
	e)	Draw	Draw pin diagram of IC LM-324.												
	f)	State	an	y two	o applications	of PLL.									
	g)	Defir	ne t	he fo	ollowing terms	s with respe	ect to	fi	ilter						
		i)	Q	facto	r										
		ii)	Ro	ll off	rate										

- a) Describe the function of intermediate stage and level shifter stage of op-amp with its block diagram.
- b) Draw and describe following op-amp based operation using log and antilog amplifier $V_0 = V_1 \times V_2$.
- c) Draw and explain the working of FM demodulator using PLL.
- d) If $V_{in1} = 5m$ Vrms and $V_{in2} = 10$ mVrms, obtain the output voltage for open loop differential amplifier. Assume op-amp is 741 and voltage swing = \pm 14V. Also sketch output waveform.

3. Attempt any <u>THREE</u> of the following:

- a) Draw the closed loop inverting amplifier using op-amp and derive the expression for its gain.
- b) Identify the given circuit and explain with input-output waveforms. Refer Fig. No. 1.



Figure No. 1.

- c) Design and draw 1st order high pass filter with cut off frequency 2 kHz and passband gain of 2.
- d) Draw circuit diagram of Bistable multivibrator using IC 555 and describe its working, working with waveforms.

Marks

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4. Attempt any THREE of the following:

- a) Suggest op-amp based circuit to convert square wave to spikes and draw the circuit with input and output waveforms.
- b) Explain with circuit diagram, the procedure to null the offset voltage in op-amp.
- c) Design and draw the circuit for the following operation using op-amp. $V_0 = -(V_1 + V_2 + V_3)$
- d) Derive the expression for output voltage of instrumentation amplifier using 3 op-amps.
- e) Determine pulse width of Monostable multivibrator using IC 555 timer for C = 0.047 μ F and R = 56 k Ω .

5. Attempt any <u>TWO</u> of the following:

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- a) Derive output equation for voltage to current converter with grounded load. Draw the circuit diagram.
- b) Draw second order high pass filter and describe its operation with frequency response characteristics. State the equations for cut off frequency and passband gain.
- c) For a schmitt trigger using op-amp, find threshold voltage V_{UTP} and V_{LTP} when $R_2 = 150 \text{ k}\Omega$, $R_1 = 100 \text{ k}\Omega$. $V_{\text{in}} = 500 \text{ mV}$, sine wave saturation voltage $= \pm 15 \text{ V}$. Also find hysteresis voltage.

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6. Attempt any <u>TWO</u> of the following:

- a) Draw and describe working of Colpitt's oscillator with neat circuit diagram. Also compare Colpitt's oscillator with Hartley oscillator with respect to
 - i) Frequency of oscillation formula
 - ii) Components used
- b) Explain the operation of sample and hold circuit with neat diagram. Draw the input-output wave forms.
- c) Identify and sketch op-amp based filter circuit to fulfill the following frequency response. Draw its ideal characteristics. Refer Fig. No. 2.



Figure No. 2.

Marks