

MODEL ANSWER

WINTER- 18 EXAMINATION

Subject Title: Television Reception

Subject Code:

17547

Important Instructions to examiners:

The answers should be examined by key words and not as word-to-word as given in the model answer scheme.

The model answer and the answer written by candidate may vary but the examiner may try to assess the understanding level of the candidate.

The language errors such as grammatical, spelling errors should not be given more Importance (Not applicable for subject English and Communication Skills.

While assessing figures, examiner may give credit for principal components indicated in the figure. The figures drawn by candidate and model answer may vary. The examiner may give credit for any equivalent figure drawn.

Credits may be given step wise for numerical problems. In some cases, the assumed constant values may vary and there may be some difference in the candidate's answers and model answer.

In case of some questions credit may be given by judgment on part of examiner of relevant answer based on candidate's understanding.

For programming language papers, credit may be given to any other program based on equivalent concept.

Q. No.	Sub Q.N.	Answer	Marking Scheme
Q.1	A)	Attempt any Three	12-Total Marks
	a)	Explain Generation of U and V signals using Adder /Subtractor circuit.	4M
	Ans:	Line nosDirect signal $N-1$ $U-jV$ $U-jV$ $(U+jV) + (U-jV) = 2U$ N $U+jV$ $N+1$ $U-jV$ $N+2$ $U+iV$ $U+iV$ $U+iV$	2M Diagram
		N + 2 U + jV and so on From video preamplifier Chrominance bandpass amplifier U + jV Delay line G3.943 μ S Delayed signal U + jV U - jV	2M Explanati on
		Chroma Bandpass Amplifier:	
		It Consist of Bandpass circuit for the selection of chroma signal. Its center frequency is equal to chroma subcarrier itself i.e. 4.43MHz. Delay Line:	
		The Object of delay line is to delay the chrominance signal by almost one line period of 64 microseconds	
		Adder & Subtractor:	
		The Chrominance amplifier feeds the chrominance signal to the ADDER, SUBTRACTOR and the DELAY LINE. The delay line feeds its output to both the	



	adder and subtractor circuit and its receive the two signals simultaneously.	
	Adder add U+jV and U-jV to get 2U and Subtractor Circuit produces a signal only of V Information i.e. 2V Whose Polarity alternates from line to line in sync with corresponding phase reversal in the encoder at the transmitting side.	
	<u>U & V Demodulator:</u>	
	The U & V Suppressed Carrier amplitude modulated components. These Correspond to the two colour difference signals and bear information about their amplitude and polarity.	
	Because of Quadrature modulation, the two modulation product signals have phase difference of +90 and -90 at any instant with respect to each other. It demodulate the incoming U & V Signals.	
b)	Draw neat block diagram of IC 7609	4M
Ans:	Note: Marks should be given if internal block diagram is made.	4M
	10K C19 Sync Sync Sync Sync Sync Separator regulator Responder Amp. and Flip-tlop Soc Responder Flip-tlop Soc Flip-tlop Soc Flip-tlop Soc Flip-tlop Soc Flip-tlop Soc Control driver Horz. osc Soc Control driver Horz. hold Responder Control C	Diagram
c)	State the 2 advantages and 2 disadvantages of remote control system	4M
Ans:	 Advantages: Audio muting option is available. TV can be programmed by remote control system. User can make their own set of selected channels. 	2M for any 2 Advantag es
	 Disadvantages: It requires line of site. Range of IR sensor is less compared to any other wireless communication components. Latest TV do not have power on/OFF button on the TV so if remote is faulty TV cannot be stay off condition means it goes in standby mode which ultimately cause loss of power. 	2M for any 2 Disadvan tages











	b)	Draw construction of trinitron colour picture tube and describe its working.	6M
	Ans:	Tube neck B G R Cathodes in-line (a) • The Trinitron or three in-line cathodes colour picture tube was	3M Diagram 3M working
		 developed by 'SONY' Corporation of Japan around 1970. It employs a single gun having three in-line cathodes. This simplifies constructional problems since only one electron gun assembly is to be accommodated. The three phosphor triads are arranged in vertical strips as in the P.I.L. tube. Each strip is only a few thousandth of a centimeter wide. A metal aperture grille like mask is provided very close to the screen. 	
		 It has one vertical slot for each phosphor triad. The grill is easy to manufacture and has greater electron transparency as compared to both delta-gun and P.I.L. tubes. The beam and mask structure, together with constructional and focusing details of the Trinitron are shown in Fig. The three beams are bent by an electrostatic lens system and appear to emerge from the same point in the lens assembly. Since the beams have a common focus plane a sharper image is obtained with good focus over the entire picture area. All this simplifies convergence problems and fewer adjustments are necessary. 	
Q. 2		Attempt any FOUR	16-Total Marks
	a)	State the merits and demerits of forward SMPS	4M
	Ans:	Note: Any other relevent Points can be considered. Advantages: • Simple Circuit • Less Cost	2M for any 2 Advantag es 2M for
		 Higher Efficiency The main advantage of the forward SMPS is greater efficiency than linear regulators because the switching transistor dissolves little power when working as a switch. 	any 2 Disadvan tages
		Disadvantages:	
		• Disadvantages of SMPS include the generation of high-amplitude,	







Ans:	 Explanation: Switches S1 and S2 represents two transistors that are necessary for transfer of energy from unregulated source Vi to the load circuit. The two transistor conduct alternately by opposite polarity pulses from the pulse width modulator. Thus D1 and D2 conduct alternately as in a conventional full-wave rectifier circuit. The choke Lo and filter capacitor Co store energy to maintain steady voltage across the load. The feedback circuit operates in the same manner to actuate the PWM in such a way that conduction periods of switching transistors vary in accordance with variation in 	2M Diagram 2M Working
	load current thus maintains output voltage constant at V ₀ .	
d)	Explain the role of microcontroller in TV receiver	4M
Ans:	Explanation:The main thrust of all microcontroller is based control system is channel selection and fine tuning. For this, frequency synthesizer is used which enables crystal accuracy of the local oscillator frequency for all channels. Several facilities that become available in microcontroller controlled system include.• Auto seek programming• Manual programming• Digital 'LED' display of the selected channel• Operation with or without remote control• AFT control and• Audio mute during channel hunting.• For above reasons microcontroller in TV receiver is used.	4M 4M
e)	State the features and control available in plasma TV	4M
	 Controls available in plasma 1 v (Any 4) i. Change viewing modes ii. Use picture-in-picture (PIP) mode iii. Use picture-on-picture (POP) mode iv. Change plasma TV tuner settings v. Set the sleep timer vi. Adjust sound controls vii. Use the V-Chip (Parental Guide) controls viii. Adjust the picture 	2111



	 Features of plasma TV: (Any 4) There is no flicker as all the phosphor excited pixels react at the same time during one frame of scanning. There is also no back light and no protection of any kind. As , such the light emitting phosphors result in bright pictures with rich colors and wide viewing angle. Though plasma screens are thin, they are heavy and consume lot of power. These are also fragile and often need professional help to install them. Capable of producing deeper blacks allowing for superior contrast ratio. Wider viewing angles than those of LCD. Less visible motion blur. Superior uniformity. Unaffected by clouding from the polishing process. Less expensive for the buyer per square inch than LCD 	2M
f) Ans:	Draw block diagram of remote control transmitter and state function of each block	4M 2M
	Image: second	2M Explanati on



		 Explanation:- The pulse output (high or low) on depression of any one key button activates special individual circuitry in the encoder. It produces a distinctive pulse train which modulates the crystal controlled 48 KHz carrier. After due amplification it is fed to the LED driver which passes proportionate current pulses through the two LEDS, D1 and D2,. The emission of radiation in the infrared region causes transmission of the modulated wave towards the remote controlled receiver when the unit is aimed at it. In addition, the modulated 48 KHz output from Q1 is rectified by D4 and lights the function indicator LED (D3) located on right hand top of the transmitter panel. This LED shows that a function button has been depressed. If it fails to light, it indicate that the battery needs replacement. 	
Q. 3	A)	Attempt any FOUR	16-Total
	a)	Describe diode split technique for the generation of EHT voltage	Marks 4M
	Ans:	Ferroxide 47K	2M
		 Section 3 Section 2 Section 2 Becilion 1 Focus potential Focus potential Focus potential Focus potential Section 1 The Diode split technique is three layer secondary windings are shown wound round the ferroxide core of the L.O.T While the three section are shown separately, in actual practice these are wound one above the other and are thus concentric. Each winding is identical to the other and has the same number of turns. The same magnitude of voltage will therefore be induces in each section every time the flyback derived input pulse gets applied to the primary winding . Because of the close proximity of individual layers, an inter-layer capacitance exists between each of them. It is indicated in the diagram by capacitors in dotted – chain form because these are no physical capacitors. If a diode is 	Diagram 2M Explanati on



 connected between the end of the layer of winding and the start of the next, the ac voltages induced in each layer can be made to charge up all the interlayer capacitances to the same voltage. Since the capacitances are effectively in series, the total voltage, appearing at the output terminal is the sum of all the voltages appearing across all of them. The diodes shown connected in series between the layers are physically embedded in the winding and form an integral part of the transformer. The three winding are so designed that voltage induced in each layer form the flyback transformer in 8.33KV. This makes the total potential equal to 25KV and forms the EHT supply source. 	
Draw block diagram of remote control receiver and state function of each block	4M
Optical coupler	2M
220V AC output TRIAC ON-OFF switch 220V AC input Remote infrared LED LED LED Gat FET Concessor and pulse Sensor preamplifier Concessor Co	Diagram 2M Explanati on
 A Schematic diagram of the remote control receiver is shown in fig. The pulse modulated infrared signals from the remote infrared transmitter are picked up by the remote infrared sensor D1. The picked up ac signal feeds into Q1,an FET amplifier, for some amplification. It is then coupled to the remote processor and pulse shaper via emitter follower Q2. The modulated signals are further amplified and then demodulated. The 	
	 connected between the end of the layer of winding and the start of the next, the ac voltages induced in each layer can be made to charge up all the interlayer capacitances to the same voltage. Since the capacitances are effectively in series, the total voltage, appearing at the output terminal is the sum of all the voltages appearing across all of them. The diodes shown connected in series between the layers are physically embedded in the winding and form an integral part of the transformer. The three winding are so designed that voltage induced in each layer form the flyback transformer in 8.33KV. This makes the total potential equal to 25KV and forms the EHT supply source.



	• The microcontroller on receipt of different inputs produces appropriate output to perform the ordered functions.	
c)	Draw schematic diagram of IC 1190 and state its function	4M
Ans:	14 15 9 Regulated AF Power supply AF Amplifier Loudspe aker IC 1190 IC 1190 From IC 1190 IF Active From FM VIF Amplifier IF Gittar	2M Diagram 2M Explanat on
	Input Circuit: The sync. Video detector in the VIF IC detects both video and intercarrier SIF signals. This signal fed to the sound system IC through an independence matching and filtering network. The signal that feeds at pin1 has 3dB bandwidth of about 150 KHz centered around 5.5MHz. Regulated Power Supply: A unique feature of the sound IC is their centrally regulated DC supply. The voltage regulator that feed independent DC voltage to the IF amplifier, FM detector, pre amplifier, DC voltage control. The audio amplifier section are fed directly from the 24V DC source.	
	 IF amplifier limiter: The FM SIF signal as it enters the sound IC is quite weak and has some amplitude variation due to noise pulse and somewhat unequal amplification at the RF and IF stages of receiver. Therefore this stage is designed to provide large gain and remove amplitude variation by limiting. <u>Active Filter:</u> The filter removes all harmonics caused by limiting action of the preceding IF amplifier section and feeds a sine wave SIF signal to the demodulator 	
	 with side bands not exceeding beyond about ±100KHz of 5.5MHz. <u>FM detector:</u> In this system a new technique known as differential peak detector is used to develop audio voltage from the frequency modulated signal <u>AF amplifier:</u> The FM detector output feeds into the pre amplifier which is of the complementary symmetry type. The speaker is connected at the junction of emitter 	
d)	and collector of the 2 output transistor through a large electrolytic capacitor.	4M



Ans:	 Explanation:- The dc operating vo connection causes L waveform of about 2 This is fed via C30 t R62 & c33 form a L output. This is the c second stage of Chr. When a black and w burst discriminator Under this condition enough to forward b Chroma amplifier st. This prevents applic demodulators. Thus 	Itage to Q9 is supplied via c A to function as a tuned auto 25V peak-to-peak to be devo to diode D10 which function PF which provides a steady olor killer voltage which is to oma signal amplifier. Thite picture is being receive & hence, no input to burst pin the color killer output fails bias transistor of the Chroma age is inhibited.	enter tap on L4. Such a bit able circuit Colour + =13.5v To second stage of chroma amplifier 201 201 201 201 201 201 201 201 201 201	2M Diagram 2M Explanati on V GB
	amplifier & hence, i	no colour noise appears on the	he black & white picture durin	g
e)	Compare plasma and LCD	display technology		4M
Áns:	Parameter	Plasma Display	LCD Display	1M Each
	Brightness	Very poor in direct sunlight without reflective design (battery powered devices)	Some panels are highly reflective, should be used in a dark environment for optimum picture quality	Point (Any 4 Points)
	Contrast	Over 1,000:1	Over 20,000:1	
	Color	Good on most newer	Excellent	
	Ghosting & Smearing	Display motion blur on models with slow response time, and the elimination	None even during fast motion, advancements in 3D have eliminated	
		technique (strobing	phosphor trailing due to	



		Response Time Environmental influences Aging Weight	backlight) can cause eye-strain 1–8 ms typical (according to manufacturer data), older units could be as slow as 35 ms Low temperatures can cause slow response, high temperatures can cause poor contrast Yes Light	the use of fast switching phosphors Sub-millisecond High altitude pressure difference may cause poor function or buzzing noises Yes Heavy, however, less	
				weight gain per size increase	
Q.4	A)	Attempt any three			Total Marks 12
	a)	Describe stereoscopic effe	ct with the aid of special gla	sses in a 3D TV	4M
	Ans:	In this method to slightly o	offset images are produced	on the screen One image is	4M
		tinted in red and The other	in cyan The Viewer wears	A class with a filter of Red	on Explanati
		one lens and a Filter op cy	an On the order lens. The F	Red filter allows red tinted in	mage on
		eve. Say right Eye. This w	a The Cyan filter allows the average of the two eves Receive se	eparate images Of the same	Juner
		object. Then the brain proc	cess the two images to prod	luce a 3-D effect.	
			• •		
		Methods			
		1. Anaglyph Method:	ightly offect images are pr	aduced on the corean One is	2000
		is tinted in red and	The other in even The View	wer wears A class with a fil	ter of
		Red on one lens an	d a Filter on cyan On the o	rder lens. The Red filter allo	
		red tinted image to	one eve. Say left Eve and '	The Cyan filter allows the c	van
		tinted Image to the	other eve. Say right Eve. T	This way, the two eves Rece	ive
		separate images Of	the same object. Then the	brain process the two image	es to
		produce a 3-D effe	et.		
		2. Polarization Metho	d:		
		Two offset images	of the same object on a TV	v screen are polarized different	ently,
		one using horizonta	al polarization and the othe	r, vertical polarization. The	
		viewer wears polar	ized glasses of appropriate	polarization for the eye. If	the
		glass of the left eye	e is of horizontal polarization	on, then that of the right eye	W111
		be of vertical polar	ization. Thus the left eye w	allow only vortically polorize	.s
		image to reach the	respective reting \	mow only vertically polariz	cu
		3 Shutter glasses Me	thod.		
		There are two activ	e glasses that shut un alter	nately. When and image me	ant
			- Susses that shut up alter	natery. When and image me	unt



	For the left eye comes ,The glass of the left eye is open and that of the right is closed. For the right eye, the left eye is closed and the right eye open's. The glasses open and close like shutters, hence the same. This two images are formed alternatively, One in the field of the left eye and another in the field of the right eye at the rate of 50 field per second	
b)	State the requirement of AGC and AFT ckt. in TV receiver	4M
Ans:	 Requirement of AGC: Automatic Gain Control (AGC) circuit varies the gain of a receiver according to the strength of the signal picked up by antenna. Useful signal strength at the receiver input terminals may vary from 50µV to 0.1V or more, depending on the channel being received and distance between transmitter and receiver. The AGC bias is a DC voltage proportional to the input signal strength. It is obtained by rectifying the video signal as available after the video detector. The AGC bias is used to control the gain of RF and IF stages in the receiver to keep the output at the video detector almost constant despite changes in the input signal to the tuner. Requirement of AFT: The AFT circuit is actually automatic frequency control (AFC) on the local oscillator in the tuner. This control aims at obtaining a picture of IF frequency of exactly 38.9MHz at the converter output. To achieve this, the IF frequency is obtained from the IF amplifier section is measured by a discriminator that forms part of the AFT control circuit 	2M Need of AGC 2M need of AFT
c)	Describe Auto stereoscope technique to create a 3D effect	4M
Ans:	Explanation:- Note: Any one method is to be considered i) Lenticular Method:- Diagram:- Left-hand side Lenticular lenses Left eye Viewer Viewer	2M Diagram 2M Explanati on.
	Explanation:- Lenticular lenses are cylindrical plastic glasses that are pasted in an array on a transparent sheet. • Thus sheet is struck on the screen of an LCD TV. The cylindrical lenses magnify the image to the viewer and convert the plain image into a cylindrical image. • Now, without wearing any special glasses,	







	Beam splitter Illuminisation Object Beam	
	Reference Object Beam (Scattered from the objet) Mirror Photographic plate	
	 A laser beam from a suitable source, say an LED is split into two beams by a prism or a special mirror one beam goes to the object and is scattered from innumerable points of the object and reaches the film. The other beam from the splitter goes directly to the film without encountering the object. This is called "reference beam." As all the points of the object are not equidistant from the film, the scattered beam coming from non-equidistant point will have phase difference with 	
	 respect to the reference beam from some point it would be in phase while from some other points out of phase. The phase, and weakening when they meet in an opposite phase. The interference pattern so obtained will produce the effect of depth. This is how holograms are prepared In case of TV, a 3D image will be registered on the surface of the target 	
-	 plate of the TV Camera by using holographic techniques. When this image is transmitted and reproduced on the display panel of the Tv receiver, it would be the hologram of the scene which this image is transmitted and reproduced on the display panel of the TV receiver, it would be the hologram of the scene which had been televised 	
d) Ans:	Draw vertical output amplifier cut. and describe its working Diagram:	4M 2M Diagram 2M Explanati on







	stage while band amplitter. If amplittles IF olliphil received from the NAW titler to a -			
	suitable level. The maximum overall gain of the IF amplifier is kent about 60dB to			
	obtain sensitivity of 100mV at 38.9 MHz. The gain of IF amplifier is controlled by			
	the AGC controlled voltage which is applied to the 3rd stage of IF amplifier			
	the AGC controlled voltage which is applied to the 3rd stage of IF amplifier.			
	• Synchronous Demodulator: A synchronous demodulator is used for detecting			
	composite video signal. It requires a dual modulated IF signal and a reference signal for detection. It consists of 2 dual differential amplifiers and 2 modulated transistors that work in push pull configuration to detect video signal output.			
	• Video Pre- Amplifier: The video pre-amplifier with noise cancellation circuit is			
	used as a buffer stage for impedance matching and feeding of signal to the succeeding			
	stages. The bandwidth of this amplifier is slightly greater than 5MHz.			
	• White spot inverter: White spots on the screen are caused by excessive current due			
	to over modulated signal. This circuit faithfully detects the noise signal and plans			
	them to safe amplitude.			
	• AGC detector & noise inverter: AGC circuit generates control voltage to limit the			
	video signal amplitude during both positive and negative noise spikes and when the			
	voltage of the signal is low. The noise inverter circuit cancels the noise signal by			
	adding inverted noise pulses detected by white spot inverter.			
b)	Draw circuit diagram of ACC amplifier and describe its working	6m		
0)	+15V	3M		
		Diagram		
	R46 33K	3M		
	H	Explanati		
	D8 R43	on		
	ISK DELAY			
	Burst pulse T100nt T100nt ACC voltage			
	Burst pulse input R45 R45 R47 amplifier			
	Burst pulse input R45 R47 amplifier			
	Burst pulse input ACC amplifier circuit. ACC amplifier circuit. ACC amplifier			
	• The amplified burst signal that become available at the output of gated burst			
	 Burst pulse input input ACC amplifier circuit. ACC amplifier circuit. ACC amplifier circuit. ACC amplifier circuit.			
	Burst pulse input ACC woltage to 1st chroma ACC voltage to 1st chroma ACC amplifier circuit. • The amplified burst signal that become available at the output of gated burst amplifier is also fed to diode D8 of the automatic colour control amplifier circuit as shown in fig.			
	 Burst pulse input ACC amplifier circuit. The amplified burst signal that become available at the output of gated burst amplifier is also fed to diode D8 of the automatic colour control amplifier circuit as shown in fig. The diode together with filter (C21, R43, and C22) acts as a HWR-cum-filter to 			
	Burst pulse input ACC amplifier etreuit. • The amplified burst signal that become available at the output of gated burst amplifier is also fed to diode D8 of the automatic colour control amplifier circuit as shown in fig. • The diode together with filter (C21, R43, and C22) acts as a HWR-cum-filter to develop a negative going dc voltage at the base of Q7, which is proportional to the			
	 Burst pulse figure fi			
	 Burst pulse from the provided signal that become available at the output of gated burst amplifier is also fed to diode D8 of the automatic colour control amplifier circuit as shown in fig. The diode together with filter (C21, R43, and C22) acts as a HWR-cum-filter to develop a negative going dc voltage at the base of Q7, which is proportional to the strength of received signal. As stated C21, R43 & C22 from a LPF to smooth any 4.43MHz variations present 			
	 Burst pulse for the p			
	 Burst pulse for any line for the control of gated burst amplifier is also fed to diode D8 of the automatic colour control amplifier circuit as shown in fig. The diode together with filter (C21, R43, and C22) acts as a HWR-cum-filter to develop a negative going dc voltage at the base of Q7, which is proportional to the strength of received signal. As stated C21, R43 & C22 from a LPF to smooth any 4.43MHz variations present in the rectified signal. The output voltage at the collector of transistor Q7 is a positive voltage which increases or decreases with the strength of Chroma signal. 			
	 Burst pulse from the strength of received signal. Acc any life circuit. Acc any life circuit.<td></td>			
	 Burst pulse figure figure figure for isot chroma input figure figure for isot control amplifier circuit as shown in fig. The diode together with filter (C21, R43, and C22) acts as a HWR-cum-filter to develop a negative going dc voltage at the base of Q7, which is proportional to the strength of received signal. As stated C21, R43 & C22 from a LPF to smooth any 4.43MHz variations present in the rectified signal. The output voltage at the collector of transistor Q7 is a positive voltage which increases or decreases with the strength of Chroma signal. This positive voltage is typically 7V under normal signal strength conditions. The resistor R44 provides an adjustable reverse bias for Q7 to delay its conduction until 			
	 Burst pulse fight filter (C21, R43, and C22) acts as a HWR-cum-filter to develop a negative going dc voltage at the base of Q7, which is proportional to the strength of received signal. As stated C21, R43 & C22 from a LPF to smooth any 4.43MHz variations present in the rectified signal. The output voltage at the collector of transistor Q7 is a positive voltage which increases or decreases with the strength of Chroma signal. This positive voltage is typically 7V under normal signal strength conditions. The resistor R44 provides an adjustable reverse bias for Q7 to delay its conduction until the Chroma signal exceeds a given threshold. 			
	 Burst pulse from the strength of received signal. Acc amplater circuit as the collector of transistor Q7 is a positive voltage which increases or decreases with the strength of Chroma signal. This positive voltage is typically 7V under normal signal strength conditions. The resistor R44 provides an adjustable reverse bias for Q7 to delay its conduction until the Chroma signal exceeds a given threshold. The potentiometer formed by resistors R46 & R47 is used to obtain correct steady 			
	 Burst pulse from the provided and the provided a			



Q.5		Attempt any Two	Total
	a)	Draw block diagram of colour TV receiver and describe the function of sweep section	Marks 16 8M
	a) Ans:	Explanation:- • The synchronizing pulses generally called _sync [*] are part of the composite video	8M 6M Diagram 2M Explanati on
		 The synchronizing pulses generally called "sync" are part of the composite video signal as the top 25 percent of the signal amplitude. The sync pulses include horizontal, vertical and equalizing pulses. There are separated from the video signal by the sync separator. The clipped line (horizontal) and field (vertical) pulses are processed by appropriate line pulse and field pulse circuitry. The sync output thus obtained is fed to the horizontal and vertical deflection oscillators to time the scanning frequencies. 	



		• As a result, picture information is in correct position on the raster. The sequence of operations is illustrated in Fig. by a block schematic diagram.	
-	b)	Draw horizontal output amplifier circuit and describe its working	8M
	Ans:	(A) (A) EHT and low voltage supplies TINE CC	4M Diagram 4M Explanati on
		Line trigger pulses Line driver Line output	
		Working :-	
		• The principle of diode spilt addition is illustrated in fig where three layers of secondary winding are shown wound round the feroxide core of LOT while the three sections are shown separately. In actual practice, these are wound one above the other and are thus concentric. Each winding is identical to other and has the same no of turns	
		 The same magnitude of voltage will be induced in each section every time the fly back derived input pules gets applied to primary winding. 	
		 Because of close proximity of individual layers and interlayer capacitance exist between each of them . It is indicated in the diagram by capacitor in dotted chain form become these are not 	
		 physical capacitor of a diode is connected between the end of layers of winding and start of the next the Ac voltage induced in each layers can be made to change. Up all the interlayer capacitance to the same voltage since the capacitance are effectively in series. 	
		 The total voltage appearing at the output terminal is the sum of all voltage appearing across all of them. The diode shown connected in series between the layers are physically embedded in 	
		 winding from an interlayer part of Transformer The three winding are so designed that voltage induced in each layer form the fly back Transformer is 8.33 Kv. This makes total potential equal to 25Kv and forms the EHT supply source. 	
·	c)	Draw block diagram of PAL-decoder and state the function of each block	8M







		demodulator.	
		• Gated burst amplifier:	
		The gated burst amplifier separates the burst pulses and amplifies them a level	
		suitable to operate the burst phase discriminator.	
		Automatic Chroma Control (ACC):	
		The magnitude of the voltage so fed back is proportional to the magnitude of	
		the burst and therefore to the amplitude of Chroma signal itself. This voltage	
		is used to control the first stage of Chroma amplifier in such way to ensure	
		constant Chroma signal amplitude.	
		Burst phase discriminator:	
		It is sensitive to burst pulses and is designed to detect any differences which	
		might exist between the phase of burst pulse and that of the reference	
		oscillator. It produces at its output a dc voltage whose magnitude and polarity	
		are proportional to the magnitude and direction of the detected phase	
		difference	
		Burst phase identifier:	
		• Burst phase identifier. This circuit is able to identify the phase relationship of the colour burst 180°	
		switch: This switch is used to periodically invert the waveform fed to the y	
		signal demodulator	
		• Colour killer control: This is just a half wave rectifier which produces a	
		steady dc potential from the succession of burst pulses. During black and	
		white transmission the dc potential is absent and hence biases the 2nd Chroma	
		amplifier to cut off state.	
06		Attempt any four.	Total
			Marks 16
	a)	State the advantages of SAW filter	4M
	Ans.	Note: Any other advantages can be considered.	1M each.
		Advantages of SAW Filter	
		1. compact packages	
		2. Light in weight	
		3. superior linear phase characteristics and rejection qualities	
		4. stable performance over temperatures	
		5 robust and reliable designs that remain stable in the field/application	
		Additionally	
		/ waitionally	
	b)	Describe the basic principle of formation of picture pixels in plasma TV	4M







Ans.	From UHF and VHF antennas			2M Diagram
	 Matching network In the VHF tuner accomplished by stages that form I must be switched The mixer input 0 tuned Circuits to switched) and a capacitance of th tuning control is correct setting of obtained on the s The IF Output from antena tuner (see fig) wh US ship channel when a VHF character 	JHF tuner Trap circuits AGC bias Ganged Uning Ganged Uning F section, the function of the Simultaneously Adjusting Part of the tuner. This mean while changing channels. Circuit is of Broad band ty be switched. Each tuned O capacitor. The resonating of e circuit plus small fixed O varied To obtain Exact pion f local Oscillator frequence creen. om the accompanying UH ha On UHF Channels. It is nose mixer stage is switched is tuned. This boosts the s nnel is tuned.	From the selection is the channel selection is the channel selection is the three or four tuned Circuits re channel selection is that three or four tuned Circuits that three or four tuned Circuits the pe and thus there are only three Circuit Consist of a coil (that is the consists of distributed Ceramic Capacitors. The fine ture and sound IF frequencies. The y is indicated by the best picture F Tuner IS low Because of weak therefore coupled to the VHF ed to act as IF amplifier When any ignal to a level normally obtained	2M Explanati on
d)	Compare CRT and Plasm	na displaytechnology		4M
Ans.	Note: Any other relevant	t point can be considered.		1M Each
	Deremeter	Plasma display	I CD display	point.
	Brightness	Very poor in direct	Better than plasma	
	Dirgituiess	sunlight		
	Power consumption	more	Less	
	contrast ratio	Over 1600:1	Over 3000:1	
	Weight	Heavy	Light	
	Backlight	Absent	present	
		Cood	Pottor than plasma	1
	Picture quality	Good	Better than plasma	



e)	State need of reference oscillator and colour killer ckt in TV receiver	4M
Ans.	Need of reference oscillator :	2M Need
	• The U and V Chroma signals are separately produced at the transmitting end by	of Ref.
	what is known as double- balanced suppressed-carrier modulator. Thus it is necessary	Oscillator
	to regenerate the subcarrier in the receiver to affect demodulation of colour difference	
	signal.	2M Need
	• The reference oscillator circuit that generates a sinusoidal output at 4.43MHz is	of Colour
	shown in figure above.	killer
	• The frequency is determined by the center-resonant frequency of the crystal (XL)	circuit.
	which in this circuit is approximately 4.43MHz.	
	Need of colour killer circuits:	
	The reception of a black-and-white television signal by a colour television receiver	
	may cause unpleasant colour noises to appear on the screen as a result of operation of	
	the associated colour signal circuitry, and thus in order to avoid this and provide	
	satisfactory reproduction of a black-and-white image, it is desirable to make the	
	bandpass amplifier inoperative upon receiving a black-and-white television signal	